# **Vincent Leung**

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## **OBJECTIVE**

Skilled semiconductor/process engineer with 2 years of experience in process improvement, sample analysis, and project management. Seeking a challenging role to apply technical expertise and contribute to the development and optimization of semiconductor manufacturing processes

#### **EXPERIENCE**

**Applied Materials** *Process Technician* 

Santa Clara, CA Jul 2021-Present

- Developed and implemented a best known method (BKM) for sample handling, resulting in improved communication and efficiency in BU Engagement Project
- Collaborated with MDP BU on the Intel RUNDA project to analyze tungsten gapfill, contributing to process optimization and product quality
- Revised job instructions and communicated with engineers on Lab IP protocol procedures, ensuring accurate sample structure depth and pattern capture
- Led swing shift team, effectively delegating samples and resolving tool issues, resulting in increase of 24 hour turnaround time (TAT) to > 85%
- Conducted training sessions for new hires on epoxy application and curing techniques, as well as the utilization of Focused Ion Beam (FIB) and Scanning Electron Microscope (SEM) for sample analysis

## **Monterey Mechanical Co.**

Oakland, CA

Project Engineer Intern

Jun 2019-Sept 2019

- Analyzed variety of drawings (structural, civil, electrical, mechanical, and PIDs) to determine piping and fittings needed to connect gas to day tank
- Revised Standard Operating Procedures (SOP) in compliance with OSHA guidelines, ensuring safe excavation at the SFPUC Oceanside project
- · Prepared detailed sectional drawings for conduit implementation and rebar placement in duct banks
- Resolved gas purge issue by submitting isometric drawings detailing the connection between the gas tank and burner

## SKILLS

Software: Integrated Framework for Automated Software Testing (iFast), Python, MATLAB, LaTeX

Analytics: Aspen Plus (Process Simulation Software), Excel, ImageJ

Language: English, Cantonese (Limited Working Proficiency)

# **EDUCATION**

#### **University of California, Davis**

Davis, CA

Bachelor of Science, Chemical Engineering

Sept 2016-Jun 2020

2020 Sandia Engineering Design Award Recipient